	Application No.	Applicant(s)
Notice of Allowability	10/087,228	ANNADURAI ET AL.
	Examiner	Art Unit	
	John Pezzlo	2662	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *application filed 27 February 2002*.

2. ☒ The allowed claim(s) is/are *1-26*.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some*   c)☐ None   of the:

        1. ☐ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a)☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1)☐ hereto or 2)☐ to Paper No./Mail Date _____ .

    (b)☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of

        Paper No./Mail Date _____ .

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☐ Examiner's Amendment/Comment

8. ☒ Examiner's Statement of Reasons for Allowance

9. ☒ Other *See Continuation Sheet*.

JOHN PEZZLO
PRIMARY EXAMINER

Continuation of Attachment(s) 9. Other: Drawings filed 27 February 2002 are accepted by the examiner.

DETAILED ACTION

Allowable Subject Matter

Claims 1-26 are allowable over the prior art of record.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance: Applicants have claimed uniquely distinct features in the instant invention, which are not found in the prior art, either singularly or in combination. Each independent claim identifies the following uniquely distinct features;

1.   Regarding claim 1 – A method for processing data packets within a communication system comprising: receiving a first data packet having a plurality of bytes of data, determining whether the first data packet ends with an invalid byte of data, if an invalid byte of data is detected in the first data packet, dropping the invalid byte of data, receiving a second data packet having a plurality of bytes of data, shifting a byte of data from the second packet in place of the dropped invalid byte of data in the first packet, and continue shifting data one byte at a time in the second data packet.

2.   Regarding claim 4 – A method for processing data packets within a communication system, the method comprising: receiving a first data packet in the communication system, the first packet ending with a valid byte and an invalid byte of data, dropping the invalid byte of

data, receiving a second data packet that begins with a first valid byte of data, and concatenating

the valid byte of the first data packet with the first valid byte of the second data packet.

3.   Regarding claim 15 – A concatenation circuitry, comprising: a first multiplexing logic

circuitry having a data output port, a data select port, and first and second data input ports, the

data select port for selectively coupling any one of the first, or second data input ports to the data

output port; a first register, having a data input port for receiving input data and a data output

port for coupling to the first data input port of the multiplexing logic circuitry, a second register

having a data input port for receiving input data, and for coupling to the data input port of the

first register, the second register having a data output port for coupling to the second data input

port of the multiplexing logic circuitry, a third register having a data input port for coupling to

the data output port of the first multiplexing logic circuitry, and a data output port for outputting

data, a fourth register having a data input port, and a data output port for outputting data, and a

second multiplexing logic circuitry having a first data input port for coupling to the data output

port of the second register, the second multiplexing logic circuitry having a second data input

port for coupling to the input port of the second register, the second multiplexing logic circuitry

having a data output port for coupling to the data input port of the fourth register, the second

multiplexing logic circuitry having a data select port for selectively coupling either the first data

input port or the second data input port with the data output port of the second multiplexing logic

circuitry.

4.   Regarding claim 18 – A logic circuitry for processing data packets within a

communication system such that data packets contain valid bytes of data, the circuitry

comprising: logic circuitry for determining that a first packet ends with a valid byte and an

invalid byte of data, logic circuitry for dropping the invalid byte of data, logic circuitry for determining that a second data packet that begins with a valid byte of data, and logic circuitry for concatenating the valid byte of the first data packet with the valid byte of the second data packet.

5.   Regarding claim 23 - A circuit for processing packets of data wherein a packet includes one or more words each having a first byte and a second byte, the circuit comprising: a first register coupled to receive and store the first byte of each word, a second register coupled to receive and store the second byte of each word, a third register coupled to the first register and configured to receive and store contents of the first register in response to a clock signal, a fourth register coupled to the second register and configured to receive and store contents of the second register in response to the clock signal, and a multiplexing circuit coupled to the third and fourth registers and configured to selectively rearrange pairing of bytes.

The closest prior art, either singularly or in combination, fail to anticipate or render the above limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Claims 1-26 being allowable, **Prosecution On The Merits Is Closed** in this application.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

1.   Thomas et al. (US 5,862,206) discloses a method and apparatus for performing raw cell status report frequency mitigation on transmit in a network node.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to John Pezzlo whose telephone number is (571) 272-3090. The examiner can normally be reached on Monday to Friday from 8:30 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou, can be reached on (571) 272-3088. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-2600.

Any response to this action should be mailed to:

    Commissioner of Patents and Trademarks

    Washington, D.C.

or faxed to:

    (571) 273-8300

For informal or draft communications, please label "PROPOSED" or "DRAFT"

Hand delivered responses should be brought to:
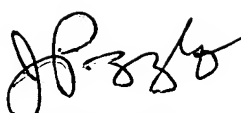
    Jefferson Building

    500 Dulany Street

Alexandria, VA.

John Pezzlo

24 January 2006

JOHN PEZZLO
PRIMARY EXAMINER